



"state machine" circuit configuration net

Search

[Advanced Scholar Search](#)  
[Scholar Preferences](#)  
[Scholar Help](#)

**Scholar**

Results 11 - 20 of about 2,810 for "state machine" circuit configuration net. (0.16 seconds)

The Transmogrifier C Hardware Description Language and Compiler for FPGAs - group of 7 »

D Galloway - Proceedings of IEEE Workshop on FPGAs for Custom Computing ..., 1995 - doi.ieeecs.org  
 ... The PRISM-II **Configuration** compiler has been designed ... number of times, and requires a sequential **circuit**. The compiler builds a sequential **state machine** as the ...  
[Cited by 75](#) - [Related Articles](#) - [Web Search](#)

Petri net synthesis and analysis of a flexible manufacturing systemcell - group of 2 »

M Zhou, K McDermott, PA Patel - Systems, Man and Cybernetics, IEEE Transactions on, 1993 - ieeexplore.ieee.org  
 ... the subnets, for example, all subnets being **state-machine** like nets ... since the physical layout or **configuration** of an ... Since every **circuit** in this subnet has at ...  
[Cited by 39](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

... Configuration of a Run-time Reconfigurable Custom Computing Integrated Circuit for DSP Applications - group of 2 »

MF Cherbaka - 1996 - scholar.lib.vt.edu  
 ... 55 4.6 Crossbar node **state machine** design. ... For example, the clock generation **circuit** was tested extensively for reliability and timing. ... 1.1.2 **Configuration** ...  
[Cited by 5](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

An Open Platform for Development of Network Processing Modules in Reprogrammable Hardware - group of 7 »

JW Lockwood - IEC DesignCon'01 - cs.wustl.edu  
 ... services and com- plex port filtering mechanisms are needed to protect **net-** works from ... RAD Virtual **Circuit** ... Figure 4: Network Interface Device **Configuration** ...  
[Cited by 31](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

Static and dynamic configurable systems - group of 10 »

E Sanchez, M Sipper, JO Haenni, JL Beuchat, A ... - Computers, IEEE Transactions on, 1999 - ieeexplore.ieee.org  
 ... parts: a control unitDa finite **state machine** that handles ... implemented by means of a Xilinx XC4003 **circuit**. Its **configuration** is fixed: 16 different sequences ...  
[Cited by 58](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

Low power pass-transistor logic and application examples - group of 3 »

K Taki, BY Lee - Electronics and Communications in Japan(Part III Fundamental ..., 1998 - doi.wiley.com  
 ... Pass-transistor logic is considered to offer an inter- esting **circuit configuration** that may achieve lower power consumption, higher speed, and smaller chip ...  
[Cited by 5](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

Introducing Discrete-Event Control Concepts and State-Transition Methodology into Control System ....

I I INTRODUCTION - IEEE TRANSACTIONS ON EDUCATION, 1994 - ieeexplore.ieee.org  
 ... 1. The basic system **configuration**. ... language by incor- porating the concept of a **state machine** [6], [17 ... This is a technique that uses relay **circuit** diagrams as a ...  
[Cited by 5](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)